

Docket No. 211200US2

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: Toshitada SAITO

FILING DATE: Herewith

FOR: ONE-CHIP SYSTEM LARGE-SCALE INTEGRATED CIRCUIT INCLUDING PROCESSOR CIRCUIT  
AND ITS PERIPHERAL CIRCUITS

**LIST OF INVENTOR'S NAME AND ADDRESS**

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

Listed below is the name and address of the inventor for the above-identified patent application.

Toshitada SAITO

YOKOHAMA-SHI, JAPAN

A declaration containing all the necessary information will be submitted at a later date.

Respectfully Submitted,

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APPLICATION DATA SHEET

APPLICATION INFORMATION

Application Type:: REGULAR  
Subject Matter:: UTILITY  
CD-ROM or CD-R?:  
Title:: ONE-CHIP SYSTEM LARGE-SCALE INTEGRATED  
CIRCUIT INCLUDING PROCESSOR CIRCUIT AND  
ITS PERIPHERAL CIRCUITS  
Attorney Docket Number:: 211200US2  
Total Drawing Sheets:: 11

INVENTOR INFORMATION

Inventor Authority Type:: INVENTOR  
Status:: FULL CAPACITY  
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REPRESENTATIVE INFORMATION

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FOREIGN PRIORITY INFORMATION

Country:: JAPAN  
Application Number:: 2000-208601  
Filing Date:: 07/10/00

ASSIGNMENT INFORMATION

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